



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Kazumasa TANIDA et al.

Docket No.: AI 427NP

Application No.: 10/594,561

Confirmation No.: 5593

Filed: September 27, 2006

Art Unit: 2826

For: SEMICONDUCTOR DEVICE WITH A  
SEMICONDUCTOR CHIP CONNECTED IN  
A FLIP CHIP MANNER

Examiner: Teresa M. Arroyo

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Upon review of the Information Disclosure Statement filed on September 7, 2010, it was noted that, due to a typographical error, one of the reference (JP2002-298768A) listed on the Form PTO/SB/08 was incorrect. A new Form PTO/SB/08 with the corrected Japanese reference (JP2002-289768A) is attached. Please substitute the new Form PTO/SB/08 for the original one.

September 17, 2010  
Date

Respectfully submitted,

By

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ALP/pq